



Welcome  
United States Patent and Trademark Office


[Help](#) [FAQ](#) [Terms](#) [IEEE Peer Review](#)
[Quick Links](#)[» Search Results](#)

## Welcome to IEEE Xplore®

- ☐ Home
- ☐ What Can I Access?
- ☐ Log-out

## Tables of Contents

- ☐ Journals & Magazines
- ☐ Conference Proceedings
- ☐ Standards

## Search

- ☐ By Author
- ☐ Basic
- ☐ Advanced

## Member Services

- ☐ Join IEEE
- ☐ Establish IEEE Web Account
- ☐ Access the IEEE Member Digital Library

## IEEE Enterprise

- ☐ Access the IEEE Enterprise File Cabinet

Print Format

Your search matched **43** of **1053485** documents.

A maximum of **500** results are displayed, **15** to a page, sorted by **Relevance** in **Descending** order.

**Refine This Search:**

You may refine your search by editing the current search expression or entering a new one in the text box.


☐ Check to search within this result set
**Results Key:**

**JNL** = Journal or Magazine   **CNF** = Conference   **STD** = Standard

### 1 Complexity analysis of multicarrier and single-carrier systems for very high-speed digital subscriber line

*Byonghyo Shim; Shanbhag, N.R.;*

Signal Processing, IEEE Transactions on [see also Acoustics, Speech, and Signal Processing, IEEE Transactions on] , Volume: 51 , Issue: 1 , Jan. 2003

Pages:282 - 292

[\[Abstract\]](#)   [\[PDF Full-Text \(560 KB\)\]](#)   **IEEE JNL**

### 2 Design and evaluation of an architecture for a digital signal processor for instrumentation applications

*Fellman, R.D.; Kaneshiro, R.T.; Konstantinides, K.;*

Acoustics, Speech, and Signal Processing [see also IEEE Transactions on Signal Processing], IEEE Transactions on , Volume: 38 , Issue: 3 , March 1990

Pages:537 - 546

[\[Abstract\]](#)   [\[PDF Full-Text \(896 KB\)\]](#)   **IEEE JNL**

### 3 A vector architecture for higher-order moments estimation

*Alves, J.C.; Puga, A.; Corte-Real, L.; Matos, J.S.;*

Acoustics, Speech, and Signal Processing, 1997. ICASSP-97., 1997 IEEE International Conference on , Volume: 5 , 21-24 April 1997

Pages:4145 - 4148 vol.5

[\[Abstract\]](#)   [\[PDF Full-Text \(332 KB\)\]](#)   **IEEE CNF**

### 4 A low power DSP engine for wireless communications

*Verbauwhede, I.; Touriguan, M.; Gupta, K.; Muwafi, J.; Yick, K.; Fettweis, G.;*

VLSI Signal Processing, IX, 1996., [Workshop on] , 30 Oct.-1 Nov. 1996

Pages:471 - 480

[\[Abstract\]](#)   [\[PDF Full-Text \(508 KB\)\]](#)   **IEEE CNF**

### 5 ASAP-a 2D DFT VLSI processor and architecture

*Mellott, J.D.; Lewis, M.; Taylor, F.; Coffield, P.;*  
Acoustics, Speech, and Signal Processing, 1996. ICASSP-96. Conference  
Proceedings., 1996 IEEE International Conference on , Volume: 6 , 7-10 May 1996  
Pages:3280 - 3283 vol. 6

[\[Abstract\]](#) [\[PDF Full-Text \(376 KB\)\]](#) [IEEE CNF](#)

---

**6 Serial/Parallel architectures for area-efficient vector multiplication**

*Smith, S.; Denyer, P.;*  
Acoustics, Speech, and Signal Processing, IEEE International Conference on  
ICASSP '87. , Volume: 12 , Apr 1987  
Pages:539 - 542

[\[Abstract\]](#) [\[PDF Full-Text \(152 KB\)\]](#) [IEEE CNF](#)

---

**7 A high-speed programmable architecture using barrel shifters for FFT and digital filtering**

*Ramamoorthy, P.; Chen, T.;*  
Acoustics, Speech, and Signal Processing, IEEE International Conference on  
ICASSP '86. , Volume: 11 , Apr 1986  
Pages:2135 - 2138

[\[Abstract\]](#) [\[PDF Full-Text \(94 KB\)\]](#) [IEEE CNF](#)

---

**8 A merged multiplier-accumulator for high speed signal processing applications**

*Fayed, A.A.; Bayoumi, M.A.;*  
Acoustics, Speech, and Signal Processing, 2002. Proceedings. (ICASSP '02). IEEE  
International Conference on , Volume: 3 , 13-17 May 2002  
Pages:III-3212 - III-3215 vol.3

[\[Abstract\]](#) [\[PDF Full-Text \(494 KB\)\]](#) [IEEE CNF](#)

---

**9 A low-power programmable DSP core architecture for 3G mobile terminals**

*Kumura, T.; Ishii, D.; Ikekawa, M.; Kuroda, I.; Yoshida, M.;*  
Acoustics, Speech, and Signal Processing, 2001. Proceedings. (ICASSP '01). 2001  
IEEE International Conference on , Volume: 2 , 7-11 May 2001  
Pages:1017 - 1020 vol.2

[\[Abstract\]](#) [\[PDF Full-Text \(376 KB\)\]](#) [IEEE CNF](#)

---

**10 A novel multirate adaptive FIR filtering algorithm and structure**

*Cheng-Shing Wu; An-Yeu Wu;*  
Acoustics, Speech, and Signal Processing, 1999. ICASSP '99. Proceedings., 1999  
IEEE International Conference on , Volume: 4 , 15-19 March 1999  
Pages:1849 - 1852 vol.4

[\[Abstract\]](#) [\[PDF Full-Text \(272 KB\)\]](#) [IEEE CNF](#)

---

**11 An enhanced floating point coprocessor for embedded signal processing and graphics applications**

*Hinds, C.N.;*  
Signals, Systems, and Computers, 1999. Conference Record of the Thirty-Third  
Asilomar Conference on , Volume: 1 , 24-27 Oct. 1999  
Pages:147 - 151 vol.1

[\[Abstract\]](#) [\[PDF Full-Text \(352 KB\)\]](#) [IEEE CNF](#)

---

**12 Low-energy heterogeneous digit-serial Reed-Solomon codecs**

*Song, L.; Parhi, K.K.; Kuroda, I.; Nishitani, T.;*

Acoustics, Speech, and Signal Processing, 1998. ICASSP '98. Proceedings of the 1998 IEEE International Conference on , Volume: 5 , 12-15 May 1998

Pages:3049 - 3052 vol.5

[\[Abstract\]](#) [\[PDF Full-Text \(456 KB\)\]](#) [IEEE CNF](#)

---

**13 Software MPEG-2 video decoder on a 200-MHz, low-power multimedia microprocessor**

*Nadehara, K.; Lieske, H.; Kuroda, I.;*

Acoustics, Speech, and Signal Processing, 1998. ICASSP '98. Proceedings of the 1998 IEEE International Conference on , Volume: 5 , 12-15 May 1998

Pages:3141 - 3144 vol.5

[\[Abstract\]](#) [\[PDF Full-Text \(360 KB\)\]](#) [IEEE CNF](#)

---

**14 A compound primitive operator approach to the realisation of video sub-band filter banks**

*Bull, D.R.; Wacey, G.; Stone, J.J.; Soloff, J.M.;*

Acoustics, Speech, and Signal Processing, 1993. ICASSP-93., 1993 IEEE International Conference on , Volume: 1 , 27-30 April 1993

Pages:405 - 408 vol.1

[\[Abstract\]](#) [\[PDF Full-Text \(292 KB\)\]](#) [IEEE CNF](#)

---

**15 A 16 b low-power-consumption digital signal processor**

*Ueda, K.; Sugimura, T.; Okamoto, M.; Marui, S.; Ishikawa, T.; Sakakihara, M.;*

Solid-State Circuits Conference, 1993. Digest of Technical Papers. 40th ISSCC., 1993 IEEE International , 24-26 Feb. 1993

Pages:28 - 29, 256

[\[Abstract\]](#) [\[PDF Full-Text \(304 KB\)\]](#) [IEEE CNF](#)

---

[1](#) [2](#) [3](#) [Next](#)

---